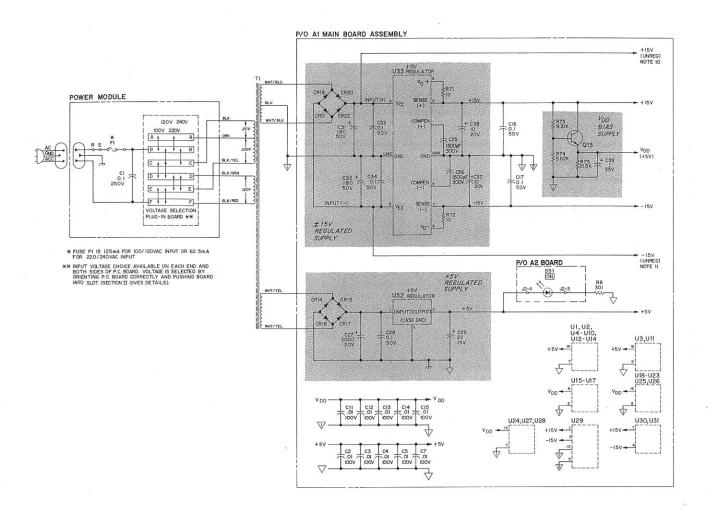
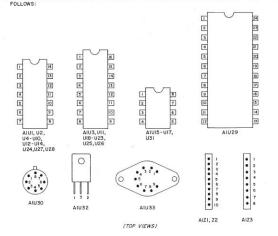


Figure 7-3 (Sheet 2). Model 59501B, Digital Processing and Analog Output Circuits, Schematic Diagram





- I. ALL RESISTORS ARE IN OHMS: 1/8W, ±1%, UNLESS OTHERWISE INDICATED. 2. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE INDICATED.
- PIN LOCATIONS FOR INTEGRATED CIRCUITS (AIUI-AIU33) AND RESISTOR NETWORKS (AIZI-AIZ3) ARE AS FOLLOWS:



- 4. THE SQUARE PLATED PADS ON THE P.C. BOARDS INDICATE ONE OF THE FOLLOWING: A. PIN 1 OF ANY 1.C. OR RESISTOR NETWORK. B. POSITIVE END OF A POLARIZED CAPACITOR. C. CATHODE OF A DIODE OR EMITTER OF A TRANSISTOR.
- 5. THE LISTEN ADDRESS FOR THE 59501B IS SPECIFIED BY THE FIVE ADDRESS SWITCHES ON THE REAR PANEL. THE SUGGESTED LISTEN ADDRESS OF "A" IS SELECTED AT THE FACTORY, HOWEVER, MY ONE OF 31 LISTEN ADDRESSES CAN BE SELECTED.
- THE REAR PANEL UNIPOLAR/BIPOLAR SWITCH (PUSH-IN, PUSH-OUT TYPE) IS SET TO THE UNIPOLAR
 POSITION (PUSHED-IN) AS SHIPPED FROM THE FACTORY.
- 7. DENOTES FRONT PANEL MARKING.
- 8. _____ DENOTES REAR PANEL MARKING.
- 9. PIN LOCATIONS FOR FET'S (Q5,Q7-Q9) AND TRANSISTORS (Q1-Q4,Q6,Q12-Q14), ARE AS FOLLOWS:



(TOP VIEWS)

- 10. +17V TO +30V MEASURED BETWEEN U33-4 AND V
- II. -I7V TO -30V MEASURED BETWEEN U33-5 AND

Figure 7-3 (Sheet 1). Model 59501B, Bias Voltage Supplies, Schematic Diagram